



SSC2500GS9

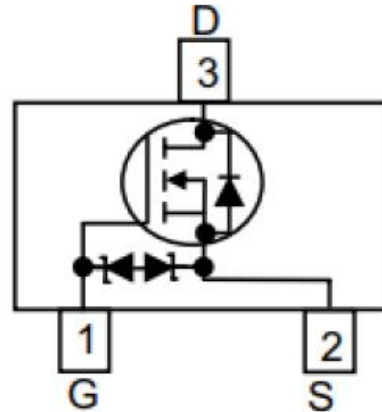
N-Channel Enhancement Mode MOSFET with ESD Protection

➤ Features

VDS	VGS	RDS(on) Typ.	ID	ESD
20V	±8V	215mR@4V5	1A	2K
		260mR@2V5		
		310mR@1V8		

➤ Pin configuration

Top view



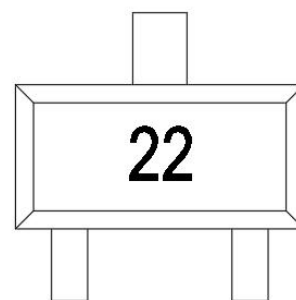
SOT723

➤ Description

This device is a N-Channel enhancement mode MOSFET which is produced with high cell density and DMOS trench technology. This device particularly suits low voltage applications, especially for battery powered circuits, the tiny and thin outline saves PCB consumption.

➤ Applications

- Replace Digital Transistor
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching cell Phones



Marking

➤ Ordering Information

Device	Package	Shipping
SSC2500GS9	SOT723	8000/Reel



➤ **Absolute Maximum Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	20	V
V_{GSS}	Gate-to-Source Voltage	± 8	V
I_D	Continuous Drain Current ^a	1	A
I_{DM}	Pulsed Drain Current ^b	2.5	A
P_D	Power Dissipation ^c	0.3	W
P_{DSM}	Power Dissipation ^a	0.17	W
T_J	Operation junction temperature	-55 to 150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range	-55 to 150	$^{\circ}\text{C}$

➤ **Thermal Resistance Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a		735	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance		416	

Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user is specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_J(\text{MAX})=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

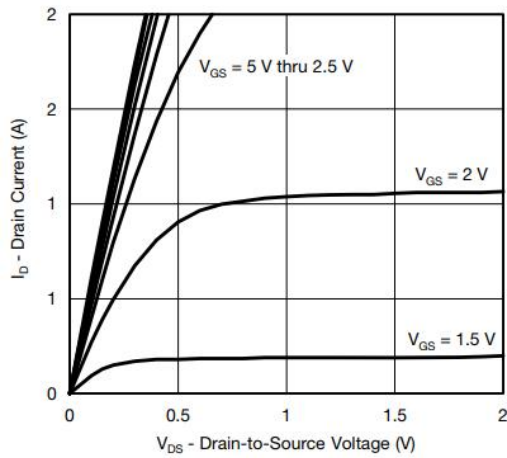


➤ **Electronics Characteristics**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

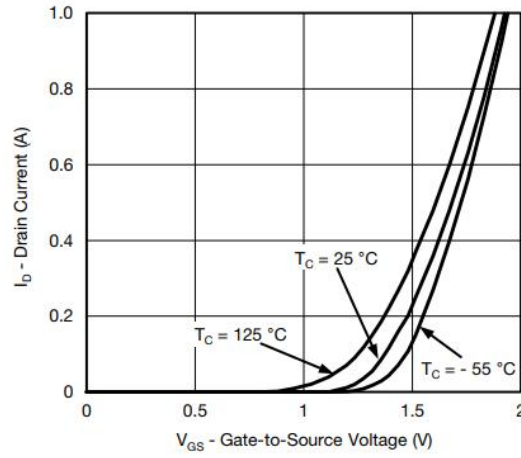
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.7	1	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=4.5V, I_D=0.5A$		215	400	mR
		$V_{GS}=2.5V, I_D=0.5A$		260	500	
		$V_{GS}=1.8V, I_D=0.35A$		310	800	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=20V, V_{GS}=0V$			1	μA
I_{GSS}	Gate-Source leak current	$V_{GS}=\pm 8V, V_{DS}=0V$			± 10	μA
G_{FS}	Forward Transconductance	$V_{DS}=10V, I_D=0.4A$		1		S
V_{SD}	Forward Voltage	$V_{GS}=0V, I_S=0.35A$			1.2	V
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V,$ $F=100KHZ$		86		pF
C_{oss}	Output Capacitance			16		
C_{rss}	Reverse Transfer Capacitance			8		
$T_{D(ON)}$	Turn-on delay time	$V_{GS}=4.5V,$ $V_{DD}=10V, R_G=6R,$ $I_D=0.45A$		22		ns
$T_{D(OFF)}$	Turn-off delay time			36		



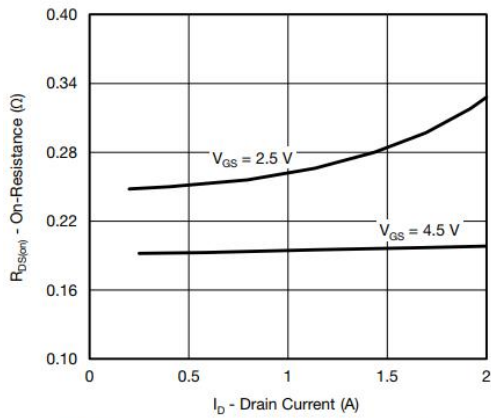
➤ **Typical Characteristics**($T_A=25^\circ\text{C}$ unless otherwise noted)



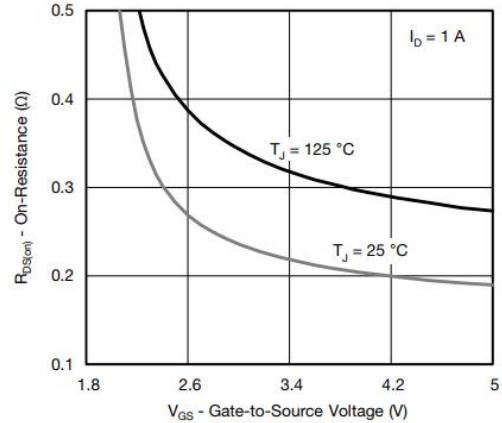
Output Characteristics



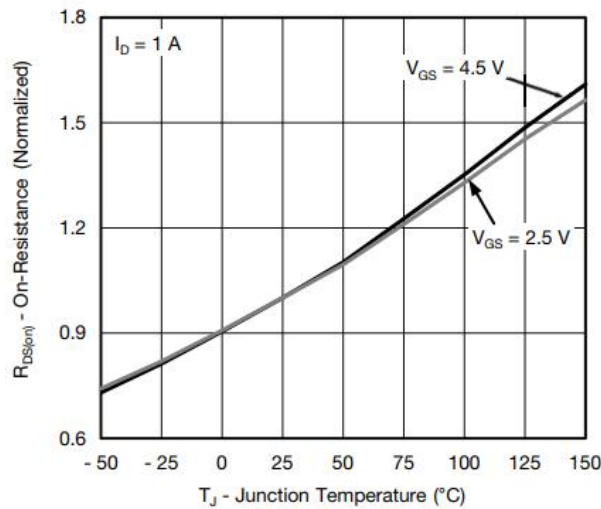
Transfer Characteristics



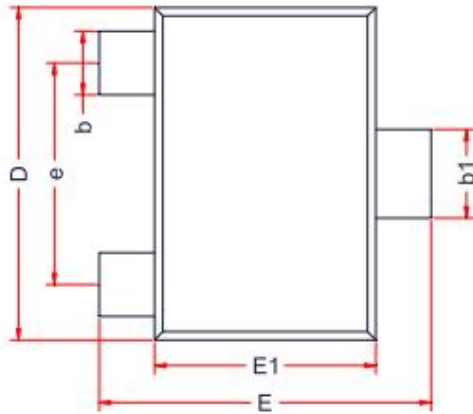
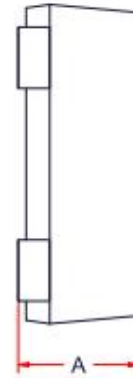
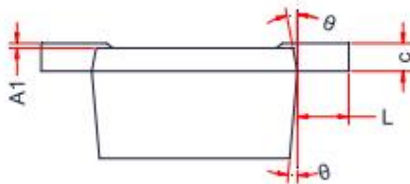
On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature

➤ Package Information
SOT-723

TOP VIEW

SIDE VIEW

SIDE VIEW

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.43	-	0.55
A1	0.00	-	0.05
c	0.08	0.13	0.18
b1	0.27	-	0.37
b	0.17	-	0.27
L1	0.15	0.20	0.25
D	1.15	1.20	1.25
E	1.15	1.20	1.25
E1	0.75	0.80	0.85
e	0.80 Ref.		
θ	7 ° Ref.		



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